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6E3205 B. Tech VI Sem. (Main & Back) Exam. May. 2013 Computer Engg. 6CS5 Embedded System Design

Time: 3 Hours

Maximum Marks: 80

Min. Passing Marks: 24

Instructions to Candidates:

Attempt any five questions, selecting one question from each unit. All questions carry equal marks. Schematic diagrams must be shown wherever necessary. Any data you feel missing suitably be assumed and stated clearly.

Units of quantities used/calculated must be stated clearly.

Use of following supporting material is permitted during examination.

UNIT – I

- What is embedded system? Explain what features should be in an embedded Q.1 (a) [8]
 - Why system designing is easy using embedded system?

[8]

OR

Explain system- on- chip (SOC) in embedded system.

[8]

What do you understand by hardware and software development environment in embedded system? [8]

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<u>UNIT - II</u>

What is RISC architecture? What features of MSP 430 make it to RISC? [8] Q.2 (a) Write a 'C' program for MSP 430 to ON the LED'S connected at (b) [8] P2.4 and P2.3. **MSP430** Microcontroller OR What is I/O interfacing? Explain seven segment and stepper motor interfacing with MSP430. [8] Discuss timers and counters of MSP430. UNIT - III How ARM processor is better for embedded system development? [8] Q.3 (a) [8] Explain current program status register. (b) OR [8] Explain pipeline stages in ARM processor. [8] Explain vector table of ARM processor. <u>UNIT - IV</u> [8] Explain 8051 flag bits and register banks. Q.4 (a) [8] What is indexed addressing mode? Explain by a suitable example.

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<u>OR</u>

	(a)	Explain architecture of 8051.	[8]
	(b)	What is interrupt? Explain interrupt programming.	[8]
		<u>UNIT – V</u>	
Q.5	(a)	What are the performance issues that affect the embedded system?	[8]
	(b)	What is sleep mode?	[8]
		<u>OR</u>	
	(a)	How smoke detector system works? Give suitable block diagram.	[8]
	(b)	How we can optimize CPU power?	[8]